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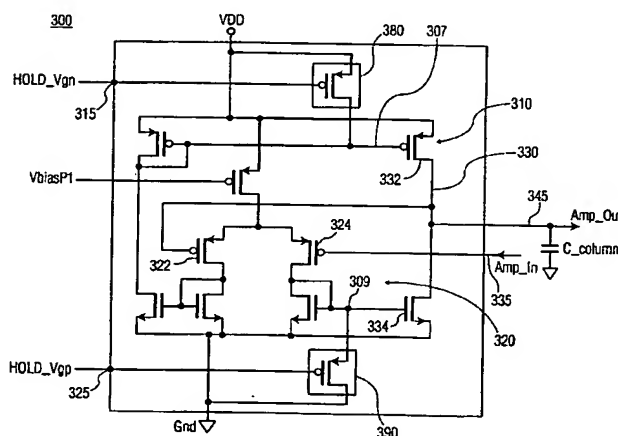
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[Continued on next page]

(54) Title: VIDEO DRIVER WITH INTEGRATED SAMPLE-AND-HOLD AMPLIFIER AND COLUMN BUFFER



(57) Abstract: A video driver includes an input stage (320, 322, 324) adapted to receive a video input signal (aMP-IN), an output stage (330, 332, 334) adapted to provide a video output signal (aMP-OUT) to charge a capacitor, a feedback path (330, 322) between the output stage and the input stage adapted to cause the video output signal to follow the video input signal, and sample-and-hold means (380, 390) for selectively turning off the output stage to disable further charging of the capacitor. In one embodiment, the output stage includes complementary transistors (332, 334) connected in series between a first supply voltage (VDD) and a second supply voltage (Gnd). The sample-and-hold means includes a first sample-and-hold switch (380) connected between a control input (307) of a first one of the complementary transistors and the first supply voltage, and a second sample-and-hold switch (390) connected between a control input (309) of a second one of the complementary transistors and the second supply voltage.

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